IN THE UNITED STATES PATENT AND TRADEMARK OFFICE.

Patent Number: 7,060,627 Issued: 6/13/2006

Name of Patentee: Micha Gutman, et al.

Title of Invention: METHOD OF DECREASING CHARGING EFFECTS IN OXIDE-NITRIDE-

OXIDE (ONO) MEMORY ARRAYS

Attention: Certificate of Corrections Branch Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR APPLICANT'S MISTAKE (37 C.F.R. section 1.323)

- It is noted that an error appears in this patent of a typographical nature or character, as more fully
 described below. It occurred in good faith. Correction thereof does not involve such changes in
 the patent as would constitute new matter or would require re-examination. A certificate of
 correction is requested.
- 2. Attached hereto is Form PTO-1050 with at least one copy being suitable for printing.
- 3. The exact page and line number where the error occurs in the application file are:

In the assignment recorded March 1, 2004, all inventors assigned their rights in the above invention jointly to Tower Semiconductor Ltd. and Matsushita Electronic Components Co., Ltd. This assignment was recorded in the USPTO on March 1, 2004 at Reel: 014387, Frame: 0973 before the issuance of the patent. The above-referenced patent is co-owned by Tower Semiconductor Ltd. and Matsushita Electronic Components Co., Ltd.

- 4. Please send the Certificate to the undersigned at Customer No.: 33726.
- Please pay the fee required by 37 C.F.R. § 1.20(a) and the processing fee required by 37 C.F.R. § 1.17(i) as follows:

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Date: November 1, 2010 /E. ERIC HOFFMAN/

Customer No.: 22888

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E. Eric Hoffman Reg. No. 38,186

Bever, Hoffman & Harms, LLP

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,060,627	Pagei_ oii_
APPLICATION NO.: 10659,042	
ISSUE DATE : June 13, 2006	
INVENTOR(S) : Micha Gutman et al.	
It is certified that an error appears or errors appear in the above-iden is hereby corrected as shown below: Correct:	tified patent and that said Letters Patent
(73) Assignee: Tower Semiconductor Ltd, Migdal Haemek (IL)	
TO:	
(73) Assignee: Tower Semiconductor Ltd., Migdal Haemek (IL); Matsush Components Co., Ltd., aka Matsushita Electric Industrial (

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Bever, Hoffman & Harms, LLP 901 Campisi Way, Suite 370

Campbell, CA 95008

This collection of information is required by 3T CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USEPTO to process) an application. Confidentially is governed by 3 U.S.C. 122 and 3T CFR 1.14. This collection is entired to idea 1.0 hours to complete, including gathering, preparing, and submitting the completed application from to the USE/TO. Time will vary depending upon the individual case. Any contents on the amount of time you require to complete file from and suggestions for reducing lines best 440,000. ON THE SER OF CONTENT OF THE CONTENT OF TH

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